

Notice of References Cited	Application/Control No. 09/976,920	Applicant(s)/Patent Under Reexamination MURPHY, CHARLES DOUGLAS	
	Examiner Chat C. Do	Art Unit 2193	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,141,674	10-2000	Unkrich et al.	708/628
	C	US-			
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	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

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	U	Naviner et al., Efficient implementation for high accuracy DCT processor based on FPGA, 1999, IEEE, page 508-511.
	V	Richard, Subexpression shring in filters using canonic signed digit multipliers, October 1996, IEEE Transactions on circuits and systems-II: analog and digital signal processing, Vol. 43, No. 10, pages 677-688.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.